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| --- | --- | --- |
| Ethernet communication | Bank 501 | Require 100Mhz 1G as option |
| Uart general purpose | Bank501 | Spare |
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|  |  |  |
| Fan control **X3** | Bank35 | Enable, speed ctl, speed measurement, Power good indication |
| Hv\_ADC spi | Bank35 | Spi ctl |
| Status in indication outputs | Bank35 | gpio |
| Relay command | Bank35 | Gpio |
|  |  |  |
| Current sense ADC spi | Bank13 | spi |
| ZCR sense ADC spi | Bank13 | spi |
|  |  |  |
| Uart RS485 **X9** | Bank 34 | Uart |
| Enables and FB PS (external units) | Bank 34 | Gpio |
|  |  |  |
| DDR3L | Bank502 | DDR 933Mhz |
|  |  |  |
| Jtag | Bank0 |  |
| Anlog ADC unipolar | Bank0 |  |
|  |  |  |
|  |  |  |
| Qspi to 128/256 Flash Memory | Bank500 | Qspi |
| Emmc 64G | Bank500 | Cannot use nand controller wiring to SD with question about CS |
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